PCI Express 8-Channel Frame Grabber Hardware Manual

Model 812 | Rev.1.0.3 | Feb 2017



Designed and manufactured in the U.S.A

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Limited warranty

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A Return Material Authorization (RMA) number must be obtained from the factory and clearly marked on the outside of the package before any equipment will be accepted for warranty work. Sensoray will pay the shipping costs of returning to the owner parts that are covered by warranty. A restocking charge of 25% of the product purchase price will be charged for returning a product to stock.

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Special handling instructions

The circuit board contains CMOS circuitry that is sensitive to Electrostatic Discharge (ESD).

Special care should be taken in handling, transporting, and installing circuit board to prevent ESD damage to the board. In particular:

- Do not remove the circuit board from its protective anti-static bag until you are ready to install the board into the enclosure.
- Handle the circuit board only at grounded, ESD protected stations.
- Remove power from the equipment before installing or removing the circuit board.

Introduction

Model 812 is a PCI-Express version of 8-channel frame/video capture device designed for the applications requiring high capture rate from multiple input video channels. It supports capturing from NTSC/PAL/SECAM video sources.

For the need of audio capturing, the Model 812 provides eight channels of monochrome audio capturing associated with eight channels of video respectively.

For each video channel, the capturing frame rate is up to 30 fps for NTSC and 25 fps for PAL/SECAM. It makes total frame/video capturing rate up to 240 fps for NTSC and 200 fps for PAL/SECAM. The capturing resolution can be from followings: D1.N (NTSC), D1.P (PAL), VGA, QVGA, QQVGA, SIF, 2SIF, 4SIF, CIF, QCIF, SQCIF, 4CIF.

For the control and/or alarming purpose, a total of 8-channel general digital I/O signals are provided.

A single +3.3V power supply through PCI-Express bus is required to power the board.

Model 812 is implemented with a single-lane (x1) PCI-Express interface. It can be plugged into any PCI-Express slot on a regular PC or a modular SBC.

Feature Summary

- PCI-Express Video/Audio Capture
- Video input: 8 individual input video channels (Composite)
- Audio input: 8 mono (monochrome) channels
- Resolution (Max): Full-D1:

NTSC: 720 x 480 @ 30 fps x 8 (Total: 240 fps) PAL: 720 x 576 @ 25 fps x 8 (Total: 200 fps)

Other supported video Resolution:

D1.N: 720 x 480 D1.P: 720 x 576 480 x 352 D.5: 2SIF: 704 x 240 704 x 480 SIF: 352 x 240 4SIF: VGA: 640 x 480 QVGA:320 x 240 QQVGA: 160 x 112 352 x 288 CIF: OCIF: 176 x 144 SQCIF: 128 x 96

4CIF: 704 x 576

Frame/Video capturing:

Raw frame capturing and/or Raw video capturing:

UYVY/Y422, YUYV/YUY2, RGB555/565

up to 30 fps x 8, for NTSC (Total: 240 fps) up to 25 fps x 8, for PAL (Total: 200 fps)

Audio capturing:

Raw audio capturing:

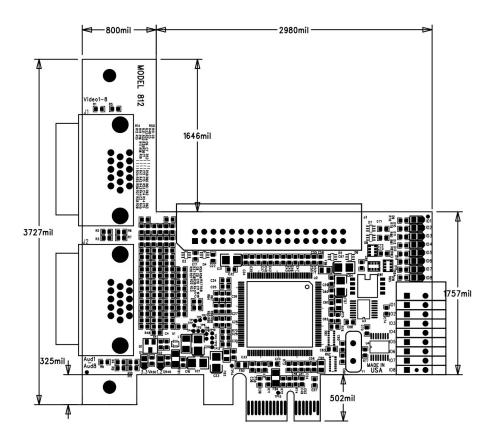
Audio sampling rate: 8 / 16 / 32 / 44.1 / 48KHz

Audio word length: 8 / 16-bit

8 digital inputs and/or 8 digital outputs: TTL signals

Reference

Board Picture and Connector Layout



Connector List

| P1 | PCI-Express Connector |
|---------|--|
| J1 | DB15: Composite Video Input for Channel-1 |
| | to 8 |
| J2 | DB15: Audio Input for 8 mono Channel-1 to 8 |
| J3 | 34-pin Connector: break-in&out, including |
| | Composite Video Input for Channel-1, 2,, |
| | and 8 |
| | Stereo/mono Audio Input for Channel-1, 2, 3, |
| | & 8 |
| | Digital Inputs for Channel-1, 2,, and 8, or |
| | Digital Outputs for Channel-1, 2,, and 8 |
| TB1~TB8 | (Optional) |
| | General Purpose Digital I/O: configurable |
| | Digital Inputs for Channel-1, 2,, and 8 |
| | or |
| | Digital Outputs for Channel-1, 2,, and 8 |

Connector Pin & Signal Definitions

PCI-Express Bus Connector: P1

| Pin - Side B | Signal | Pin - Side A | Signal |
|--------------|------------|--------------|---------|
| B1 | +12V | A1 | PRSNT#1 |
| B2 | +12V | A2 | +12V |
| B3 | Reserved * | A3 | +12V |
| B4 | Ground | A4 | Ground |
| B5 | SMCLK * | A5 | TCK* |
| В6 | SMDAT * | A6 | TDI * |
| B7 | Ground | A7 | TDO* |
| B8 | +3.3V | A8 | TMS * |
| В9 | TRST# * | A9 | +3.3V |
| B10 | +3.3VAUX | A10 | +3.3V |
| | * | | |
| B11 | WAKE# * | A11 | PWRGOOD |
| (C-Key) | | (C-Key) | |
| B12 | Reserved * | A12 | Ground |
| B13 | Ground | A13 | REFCLKP |
| B14 | HSOP0 | A14 | REFCLKN |
| B15 | HSON0 | A15 | Ground |
| B16 | Ground | A16 | HSIP0 |
| B17 | PRSNT#2 | A17 | HSIN0 |
| B18 | Ground | A18 | Ground |

Note:

* Not Connected C-Key Connector Key

Composite Video Input Connector, DB15: J1

| Pin | Signal | Pin | Signal | Pin | Signal |
|-----|--------------------------|-----|--------------------------|-----|--------|
| 1 | Composite Video In- 2 | 6 | Composite Video In- 8 | 11 | * |
| 2 | Composite Video In- 3 | 7 | Composite Video In- 7 | 12 | * |
| 3 | Composite Video In- 4 | 8 | GND | 13 | * |
| 4 | Composite Video In- 5 | 9 | GND | 14 | * |
| 5 | Composite Video In- 6 | 10 | 10 Composite Video In- | | * |

Note:

* Not Used

Audio Input Connectors, DB15: J2

| Pin | Signal | Pin | Signal | Pin | Signal |
|-----|------------|-----|------------|-----|--------|
| 1 | Audio In-2 | 6 | Audio In-8 | 11 | * |
| 2 | Audio In-3 | 7 | Audio In-7 | 12 | * |
| 3 | Audio In-4 | 8 | GND | 13 | * |
| 4 | Audio In-5 | 9 | GND | 14 | * |
| 5 | Audio In-6 | 10 | Audio In-1 | 15 | * |

Note:* Not Used

Full A/V (Video & Audio) and Digital I/O Connector: J3 (34-pin)

| Pin | Signal | Pin | Signal |
|-----|-----------------------------|-----|---------------------------------|
| 1 | Ground | 2 | Composite Video Input Channel-1 |
| 3 | Composite Video Input | 4 | Audio Input Channel-1 |
| | Channel-2 | | |
| 5 | Audio Input Channel-2 | 6 | Digital Input/Output, GPIO1 |
| 7 | Digital Input/Output, GPIO2 | 8 | Ground |
| 9 | Ground | 10 | Composite Video Input Channel-3 |
| 11 | Composite Video Input | 12 | Audio Input Channel-3 |
| | Channel-4 | | |
| 13 | Audio Input Channel-4 | 14 | Digital Input/Output, GPIO3 |
| 15 | Digital Input/Output, GPIO4 | 16 | Ground |
| 17 | Ground | 18 | Composite Video Input Channel-5 |
| 19 | Composite Video Input | 20 | Audio Input Channel-5 |
| | Channel-6 | | |
| 21 | Audio Input Channel-6 | 22 | Digital Input/Output, GPIO5 |
| 23 | Digital Input/Output, GPIO6 | 24 | Ground |
| 25 | Ground | 26 | Composite Video Input Channel-7 |
| 27 | Composite Video Input | 28 | Audio Input Channel-7 |
| | Channel-8 | | |
| 29 | Audio Input Channel-8 | 30 | Digital Input/Output, GPIO7 |
| 31 | Digital Input/Output, GPIO8 | 32 | Ground |
| 33 | +3.3V (from board) | 24 | * |

Note:* Not Used

Digital I/O Connectors: TB1 ~ TB9

| ТВ | Signal |
|----|----------------------------------|
| 1 | GPIO1 - Digital Input/Output for |
| | Channel-1 |
| 2 | GPIO2 – Digital Input/Output for |

| | Channel-2 |
|---|----------------------------------|
| 3 | GPIO3 - Digital Input/Output for |
| | Channel-3 |
| 4 | GPIO4 - Digital Input/Output for |
| | Channel-4 |
| 5 | GPIO5 - Digital Input/Output for |
| | Channel-5 |
| 6 | GPIO6 - Digital Input/Output for |
| | Channel-6 |
| 7 | GPIO7 - Digital Input/Output for |
| | Channel-7 |
| 8 | GPIO9 - Digital Input/Output for |
| | Channel-8 |
| 9 | Digital ground |

DIP Switches

Digital I/O Configuration DIP Switch: SW1

The DIP switch SW1 is used for configuring Digital I/O routing. Refer to the table below for the routing details: $\frac{1}{2} \left(\frac{1}{2} \right) = \frac{1}{2} \left(\frac{1}{2} \right) \left(\frac{1}$

| SW1-1 | SW1-2 | SW1-3 | SW1-4 | Description |
|-------|-------|-------|-------|---|
| OFF | Х | OFF | Х | Disconnect all digital I/O from/to TB1~TB8 and J3 (34-pin) connector |
| ON | Х | OFF | Х | Disconnect digital in/out GPIO1~GPIO4 from/to TB1~TB4 and J3 (34-pin) connector |
| ON | OFF | OFF | Х | Route internal GP28~GP31 on the TW6869 to the GPIO5~GPIO8; Connect them to the TB5~TB8 and J3 (34-pin) connector |
| ON | ON | OFF | Х | Route internal GP20~GP23 on the TW6869 as the GPIO5~GPIO8; Connect them to the TB5~TB8 and J3 (34-pin) connector |
| OFF | Х | ON | Х | Disconnect digital in/out GPIO5~GPIO8 from/to TB5~TB8 and J3 (34-pin) connector |
| OFF | Х | ON | OFF | Route internal GP24~GP27 on the TW6869 as the GPIO1~GPIO4; Connect them to the TB1~TB4 and J3 (34-pin) connector |
| OFF | Х | ON | ON | Route internal GP16~GP19 on the TW6869 as the GPIO1~GPIO4; Connect them to the TB1~TB4 and J3 (34-pin) connector |
| ON | Х | ON | Х | Enable the connectivity for both groups of GPIO1~4 and GPIO5~8 from/to the TB1~TB4 & TB5~TB8, respectively; Also, from/to J3 (34-pin) |
| ON | OFF | ON | OFF | Route internal GP24~GP31 on the TW6869 as the GPIO1~GPIO8; |

| | | | | Connect them to the TB1~TB8 and J3 (34-pin) connector |
|----|-----|----|-----|--|
| ON | OFF | ON | ON | Route internal GP28~GP31 on the TW6869 as the GPIO5~GPIO8; Route internal GP16~GP19 on the TW6869 as the GPIO1~GPIO4; Connect the GPIO1~GPIO8 to the TB1~TB8 and J3 (34-pin) connector |
| ON | ON | ON | OFF | Route internal GP20~GP23 on the TW6869 as the GPIO5~GPIO8; Route internal GP24~GP27 on the TW6869 as the GPIO1~GPIO4; Connect the GPIO1~GPIO8 to the TB1~TB8 and J3 (34-pin) connector |
| ON | ON | ON | ON | Route internal GP16~GP23 on the TW6869 as the GPIO1~GPIO8; Connect them to the TB1~TB8 and J3 (34-pin) connector |

LED

Power-OK indicators: D1 and D2

The LED D1 and D2 are used for indicating on-board Power-OK status.

| LED | Description |
|-----|---|
| D1 | Main Power, 3.3V Power-OK Status |
| D2 | Core Power, 1.2V Power-OK Status (for the Video Decoder Chipset TW6869, core power status monitoring) |

GPIO Status Indicators: D9 ~ D16

The LED D9, D10, ..., and D16 are used for indicating the status of the GPIO (General Purpose Digital Input/Output) pin signals, labeled as GPIO1, GPIO2, ..., and GPIO8 on the board, and directly connected to the TB1 \sim TB8 and J3 (34-pin) connector's digital in/out pins. A logic '0' (low) turns the LED on and a logic '1' (high) turns it off.

| LED | Lable | Signal |
|-----|-------|---|
| D9 | GPIO1 | Status of GPIO1 ($^{\prime}$ 0' \rightarrow On and '1' \rightarrow Off) |
| D10 | GPIO2 | Status of GPIO2 ('0' \rightarrow On and '1' \rightarrow Off) |
| D11 | GPIO3 | Status of GPIO3 ('0' \rightarrow On and '1' \rightarrow Off) |
| D12 | GPIO4 | Status of GPIO4 ('0' \rightarrow On and '1' \rightarrow Off) |
| D13 | GPIO5 | Status of GPIO5 ('0' \rightarrow On and '1' \rightarrow Off) |
| D14 | GPIO6 | Status of GPIO6 ('0' \rightarrow On and '1' \rightarrow Off) |
| D15 | GPIO7 | Status of GPIO7 ('0' \rightarrow On and '1' \rightarrow |

| | | Off) | |
|-----|-------|-----------------|--|
| D16 | GPIO8 | Status of GPIO8 | ('0' \rightarrow On and '1' \rightarrow |
| | | Off) | |

Specifications

| Video Formats | NTSC, PAL, SECAM | | |
|--------------------------------------|---|--|--|
| Video Inputs | 8 input channels, simultaneously: | | |
| | 8 Composite video, via DB15 connector, 75 Ohms; | | |
| | Or, via a 34-pin connector, 75 Ohms | | |
| Audio Inputs | 8 input channels, simultaneously: | | |
| | 8 mono for each channel, via DB15 connector; | | |
| | Or, via a 34-pin connector, 75 Ohm | | |
| | Signal level: Line-in level, +/- 1.0V | | |
| Capturing Mode | Raw: | | |
| | UYVY/Y422, YUYV/YUY2, RGB555/RGB565 | | |
| Capture rate | Up to: 240 (30x8) frames/sec for NTSC | | |
| 200 (25x8) frames/sec for PAL | | | |
| Resolution | Up to Full-D1: | | |
| | NTSC: 720x480 PAL: 720x576 | | |
| | Supported: | | |
| | D1.N: 720x480 D1.P: 720x576 D.5: | | |
| | 480x352 | | |
| | SIF: 352x240 2SIF: 704x240 4SIF: | | |
| | 704x480 VGA: 640x480 OVGA: 320x240 OOVGA: | | |
| | VGA: 640x480 QVGA: 320x240 QQVGA: 160x112 | | |
| | CIF: 352x288 QCIF: 176x144 SQCIF: | | |
| | 128x96 | | |
| | 4CIF: 704x576 | | |
| Digital I/O | | | |
| Digital 1/0 | configurable inputs/outputs | | |
| | via a 34-pin break-in/out connector | | |
| via 8 I/O terminal blocks (optional) | | | |
| Bus | PCI-Express lane x1: Compliant with | | |
| | PCI-Express Base Specification Revision 1.1 and 2.0 | | |
| Power | <1W, +3.3V @ 300mA | | |
| Temperature | 0 - 70 C | | |
| Board Size | 4.2" x 4" (107mm x 100mm) | | |

Appendix

Appendix A: Cable 812C1 ← → 812 A/V Break-in Connector Mapping

Please note that the supplied Cable 812C1 (DB15-to-BNC adapting cable for Model 812) labeling for input channel numbers does not match the 812's A/V (Video and/or Audio) channel order.

The chart given below shows the correct mapping of the Cable 812C1 \leftarrow > 812 A/V channel numbers:

| A/V Channel No. | Label on Cable |
|-----------------|----------------|
| 1 | Video - 9 |
| 2 | Video - 14 |
| 3 | Video - 13 |
| 4 | Video - 12 |
| 5 | Video - 11 |
| 6 | Video - 10 |
| 7 | Video - 15 |
| 8 | Video - 16 |